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APPLICANTS Yoshihiro Ito, Nagaokakyo-shi, JAPAN; Michio Kadota, Kyoto, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/07055 06/04/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-172301 06/13/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 19
Verified and Acknowledged Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 2		
ADDRESS Joseph R Keating Keating & Bennett 10400 Eaton Place Suite 312 Fairfax , VA 22030				
TITLE Semiconductor device and method for manufacturing semiconductor device				
FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	